

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	"6654714" .pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:17
S2	54	GENTILE-R GENTILE-ROBERT GENTILE-ROBERT-A GENTILE-ROBERT-J GENTILE-ROBERT-JAMES GENTILE-ROBERT-L GENTILE-ROBERT-S SCHAFF-TRAVIS SCHAFF-TRAVIS-J SCHAFF-T-J	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 13:27
S3	6	SCHAFF-TRAVIS SCHAFF-TRAVIS-J SCHAFF-T-J	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 15:44
S4	50	GENTILE-R GENTILE-ROBERT GENTILE-ROBERT-A GENTILE-ROBERT-J GENTILE-ROBERT-JAMES GENTILE-ROBERT-L GENTILE-ROBERT-S	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 14:02
S5	2	S4 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 13:28
S6	38	((add adding select selecting) same (compatib\$5 with (processor cpu))) same ((multi or parallel) with (processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 15:49
S7	9	S6 and ((processor cpu) with (id identif\$3 identit\$3 tag\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:20
S8	2	"6516373" .pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 14:14

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S9	20	("5428806" "5625802" "5761479" "5839584" "5848250" "5862351" "5884091" "5918023" "5983297" "6052794").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 14:15
S10	13	("5574809" "5586304" "5671435" "5761479" "5852722" "5960189" "5974258").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 15:45
S11	79366	((processor or cpu) with (id or identif\$3 or identit\$3 or tag\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 15:49
S12	184	S11 and ((compatib\$5 with (processor or cpu))) same ((multi or parallel) with (processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 15:50
S13	6	S11 same ((compatib\$5 with (processor or cpu))) same ((multi or parallel) with (processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:29
S14	292	cpuid	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:29
S15	3	cpuid with compatib\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:30
S16	318	((processor or cpu) with (id or identif\$3 or identit\$3 or tag\$4)) with compatib\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:32
S17	4	S16 same ((multi or parallel) with (processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/24 16:32
S18	233	multi\$1processor and ((processor or cpu) same (compatib\$5) same (test\$4 or check\$4 or verif\$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:19

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S19	92	multi\$1processor and ((processor or cpu) same ((compatib\$5) with (test\$4 or check\$4 or verif\$6)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:19
S20	41	multi\$1processor and ((processor or cpu) with ((compatib\$5) with (test\$4 or check\$4 or verif\$6)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:23
S21	23	S20 and ((processor or cpu) with (id or identif\$3 identit\$3 or tag\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:40
S22	41	multi\$1processor and (((processor or cpu) with (compatib\$5)) with (test\$4 or check\$4 or verif\$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:24
S23	1	multi\$1processor and (((processor or cpu) adj (compatib\$5)) with (test\$4 or check\$4 or verif\$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:39
S24	2	"6289391".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:35
S25	115	multi\$1processor and ((processor or cpu) adj (compatib\$5))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:39
S26	108	S25 and ((processor or cpu) with (id or identif\$3 identit\$3 or tag\$4 or type or info or information))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:41
S27	31	S25 and ((processor or cpu) adj (id or identif\$3 identit\$3 or tag\$4 or type or info or information))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:48
S28	19	S25 and ((processor or cpu) same (revision))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/25 11:48



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- #5 (((cpu <or> processor) <paragraph> (id <or> identif\*)) <and> (multi\*processor <or> (multi\* <sentence> processor)))<in>metadata)
- #6 (((cpu <or> processor) <sentence> (id <or> identif\*)) <paragraph> (multi\*processor <or> (multi\* <sentence> processor)))<in>metadata)
- #7 (((cpu <or> processor) <sentence> (id <or> identif\*)) <paragraph> (multi\*processor <or> (multi\* <sentence> processor))) <and> (select\* <or> add\*))<in>metadata)
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 Slattery, K.P.; Muccioli, J.P.; North, T.;  
[Electromagnetic Compatibility, IEEE Transactions on](#)  
 Volume 41, Issue 2, May 1999 Page(s):146 - 152  
 Digital Object Identifier 10.1109/15.765105  
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- 2. A real-time DOA-based smart antenna processor**  
 Kuchar, A.; Tangemann, M.; Bonek, E.;  
[Vehicular Technology, IEEE Transactions on](#)  
 Volume 51, Issue 6, Nov. 2002 Page(s):1279 - 1293  
 Digital Object Identifier 10.1109/TVT.2002.801737  
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- 3. Design of low-power baseband-processor for RFID tag**  
 He Yan; Hu jianyun; Li Qiang; Min Hao;  
[Applications and the Internet Workshops, 2006. SAINT Workshops 2006. International Symposium](#)  
 23-27 Jan. 2006 Page(s):4 pp.  
 Digital Object Identifier 10.1109/SAINT-W.2006.15  
[AbstractPlus](#) | Full Text: [PDF](#)(304 KB) IEEE CNF  
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- 4. An architecture framework for transparent instruction set customization in embedded proce**  
 Clark, N.; Blome, J.; Chu, M.; Mahlke, S.; Biles, S.; Flautner, K.;  
[Computer Architecture, 2005. ISCA '05. Proceedings. 32nd International Symposium on](#)  
 4-8 June 2005 Page(s):272 - 283  
 Digital Object Identifier 10.1109/ISCA.2005.9  
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- 5. Digital map integration in advanced tactical cockpits**  
 Farmer, R.G.;  
[Digital Avionics Systems Conference, 1990. Proceedings., IEEE/AIAA/NASA 9th](#)  
 15-18 Oct. 1990 Page(s):657 - 662  
 Digital Object Identifier 10.1109/DASC.1990.111363

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 Hopkins, A.B.T.; McDonald-Maier, K.D.;  
[Computers, IEEE Transactions on](#)  
 Volume 55, Issue 2, Feb. 2006 Page(s):174 - 184  
 Digital Object Identifier 10.1109/TC.2006.22  
[AbstractPlus](#) | Full Text: [PDF](#)(1392 KB) IEEE JNL  
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- Cray X-MP: the birth of a supercomputer**  
 August, M.C.; Brost, G.M.; Hsiung, C.C.; Schiffleger, A.J.;  
[Computer](#)  
 Volume 22, Issue 1, Jan. 1989 Page(s):45 - 52  
 Digital Object Identifier 10.1109/2.19822  
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- VLSI Implementation of Tausworthe random number generator for parallel processing enviro**  
 Saarinen, J.; Tomberg, J.; Vehmanen, L.; Kaski, K.;  
[Computers and Digital Techniques, IEE Proceedings-](#)  
 Volume 138, Issue 3, May 1991 Page(s):138 - 146  
[AbstractPlus](#) | Full Text: [PDF](#)(712 KB) IEE JNL
- The Mips R4000 processor**  
 Mirapuri, S.; Woodacre, M.; Vasseghi, N.;  
[Micro, IEEE](#)  
 Volume 12, Issue 2, April 1992 Page(s):10 - 22  
 Digital Object Identifier 10.1109/40.127580  
[AbstractPlus](#) | Full Text: [PDF](#)(1120 KB) IEEE JNL  
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- A study of the repeatability of conducted RF emissions**  
 Slattery, K.P.;  
[Electromagnetic Compatibility, 1995. Symposium Record, 1995 IEEE International Symposium on](#)  
 14-18 Aug. 1995 Page(s):323 - 326  
 Digital Object Identifier 10.1109/IEMC.1995.523573  
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**6. An Investigation on power plane decoupling for high speed multiprocessor boards**

Nuebel, J.; Roy, T.; Das, S.;

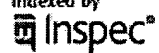
[Electromagnetic Compatibility, 1999 IEEE International Symposium on](#)

Volume 1, 2-6 Aug. 1999 Page(s):355 - 360 vol.1

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 Yang, C.-L.; Masson, G.M.;  
[Computers, IEEE Transactions on](#)  
 Volume 37, Issue 2, Feb. 1988 Page(s):175 - 181  
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[AbstractPlus](#) | Full Text: [PDF\(568 KB\)](#) IEEE JNL  
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 Digital Object Identifier 10.1109/2.19822  
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- The Mips R4000 processor**  
 Mirapuri, S.; Woodacre, M.; Vasseghi, N.;  
[Micro, IEEE](#)  
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 Saarinen, J.; Tomberg, J.; Vehmanen, L.; Kaski, K.;  
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 Volume 138, Issue 3, May 1991 Page(s):138 - 146  
[AbstractPlus](#) | Full Text: [PDF\(712 KB\)](#) IEE JNL
- A novel fiber-CAN-based real time communication system for MV-ASD systems**  
 Jue Qian; Zhengming Zhao; Haitao Zhang; Hua Bai; Liqiang Yuan;  
[Electrical Machines and Systems, 2005. ICEMS 2005. Proceedings of the Eighth International Con](#)  
 Volume 2, 27-29 Sept. 2005 Page(s):1203 - 1208 Vol. 2  
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-  6. **An investigation on power plane decoupling for high speed multiprocessor boards**  
Nuebel, J.; Roy, T.; Das, S.;  
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Volume 1, 2-6 Aug. 1999 Page(s):355 - 360 vol.1  
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-  7. **Nomad: a scalable operating system for clusters of uni- and multiprocessors**  
Pinheiro, E.; Bianchini, R.;  
[Cluster Computing, 1999. Proceedings. 1st IEEE Computer Society International Workshop on](#)  
2-3 Dec. 1999 Page(s):247 - 254  
Digital Object Identifier 10.1109/IWCC.1999.810831  
[AbstractPlus](#) | Full Text: [PDF](#)(604 KB) IEEE CNF  
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-  8. **A study of the repeatability of conducted RF emissions**  
Slattery, K.P.;  
[Electromagnetic Compatibility, 1995. Symposium Record, 1995 IEEE International Symposium on](#)  
14-18 Aug. 1995 Page(s):323 - 326  
Digital Object Identifier 10.1109/IEMC.1995.523573  
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-  9. **Performance tuning of a multiprocessor sparse matrix equation solver**  
Wu, K.Y.; Ng, P.K.H.; Jia, X.D.; Chen, R.M.M.; Layfield, A.M.;  
[System Sciences, 1995. Proceedings of the Twenty-Eighth Hawaii International Conference on](#)  
Volume 1, 3-6 Jan. 1995 Page(s):4 - 13 vol.1  
Digital Object Identifier 10.1109/HICSS.1995.375414  
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-  10. **A three-million-transistor microprocessor**  
Abu-Nofal, F.; Avra, R.; Bhabuthmal, K.; Bhamidipaty, R.; Blanck, G.; Charnas, A.; DelVecchio, P.;  
J.; Hayes, N.; Haber, G.; Hunt, J.; Kizhepat, G.; Malmay, A.; Marston, A.; Mehta, K.; Nanda, S.; Vai  
R.; Ray, A.; Reaves, J.; Rogers, A.; Rusu, S.; Shay, T.; Sidharta, I.; Tham, T.; Tong, P.; Trauben, F  
Maan, N.; Steiss, D.; Youngs, L.;  
[Solid-State Circuits Conference, 1992. Digest of Technical Papers, 39th ISSCC, 1992 IEEE Internat](#)  
19-21 Feb. 1992 Page(s):108 - 109, 257  
Digital Object Identifier 10.1109/ISSCC.1992.200435  
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overview of the system architecture**  
Hwang, J.; Wu, E.; Bell, A.; Cordell, A.; Stokes, L.; Hankins, S.;  
[Robotics and Automation, 1994. Proceedings, 1994 IEEE International Conference on](#)  
8-13 May 1994 Page(s):1286 - 1291 vol.2  
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 Masuda, S.; Hattori, T.;  
[Communications and Information Technology, 2004. ISCIT 2004. IEEE International Symposium on](#)  
 Volume 2, 26-29 Oct. 2004 Page(s):1100 - 1105 vol.2  
 Digital Object Identifier 10.1109/ISCIT.2004.1413890  
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- 2. Flexibly configurable multivision remote monitoring system**  
 Masuda, S.; Hattori, T.;  
[Circuits and Systems, 2004. MWSCAS '04. The 2004 47th Midwest Symposium on](#)  
 Volume 1, 25-28 July 2004 Page(s):1 - 301-4 vol.1  
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### CHAPTER 37 - Multiprocessor Test (mptest)

**Selecting** a different test tone exercises and tests the MP functionality in a slightly different manner. The tone option can be used to **select** the test tone ...

[docs.sun.com/source/819-6455/mptest.html](http://docs.sun.com/source/819-6455/mptest.html) - 28k - [Cached](#) - [Similar pages](#)

### IBM Personal computing support - Troubleshooting processor issues ...

Make sure the CPU and VRM parts you have installed are **compatible** with your system.

Check the IBM ServerProven **Compatibility** Web site to ensure that any ...

[www-307.ibm.com/pc/support/site.wss/document.do?Indocid=MIGR-50641](http://www-307.ibm.com/pc/support/site.wss/document.do?Indocid=MIGR-50641) - 59k -

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This is achieved by using the **cpuid** feature of the Nios II processor. ... In the Configurations list, **select** Nios II **Multiprocessor** Collection. ...

[www.altera.com/literature/tt/tt\\_nios2\\_multiprocessor\\_tutorial.pdf](http://www.altera.com/literature/tt/tt_nios2_multiprocessor_tutorial.pdf) - [Similar pages](#)

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**compatible** three-processor system. 4. **Select** File > Save to save the Block Diagram ...

writing the value of its **cpuid** control register to the OWNER field of ...

[www.imit.kth.se/courses/2B1448/docs/tt\\_nios2\\_multiprocessor\\_tutorial.pdf](http://www.imit.kth.se/courses/2B1448/docs/tt_nios2_multiprocessor_tutorial.pdf) - [Similar pages](#)

### For a description of the syntax of this configuration file, # see ...

MSR accesses are directed to a specific CPU on **multi-processor** systems. config

X86\_CPUID tristate "/dev/cpu/\*/cpuid - CPU information support" help This ...

[kernelr.traduc.org/ftp/2.6.15/a\\_traduire/arch/i386/Kconfig](http://kernelr.traduc.org/ftp/2.6.15/a_traduire/arch/i386/Kconfig) - 35k - [Cached](#) - [Similar pages](#)

### Multiprocessor arrangement including bus arbitration scheme ...

A case could also be made for merely **selecting** a more powerful processor in the ... These

**multiprocessor** configurations have also maintained **compatibility** ...

[www.freepatentsonline.com/5935230.html](http://www.freepatentsonline.com/5935230.html) - 64k - [Cached](#) - [Similar pages](#)

### Partitioned multiprocessor programming system - Patent 4564903

generating a zone value for each UPS guest by **selecting** high-order bit ... When the format

(F) bit in the ORB specifies format 0 (S/370 **compatible**) CCWs, ...

[www.freepatentsonline.com/4564903.html](http://www.freepatentsonline.com/4564903.html) - 98k - [Cached](#) - [Similar pages](#)

### [PDF] MultiProcessor Specification

File Format: PDF/Adobe Acrobat - [View as HTML](#)

For PC/AT **compatibility**, the bootstrap processor must support DOS-**compatible** FPU execution. and exception handling while running in either of the ...

[developer.intel.com/design/pentium/datashts/24201606.pdf](http://developer.intel.com/design/pentium/datashts/24201606.pdf) - [Similar pages](#)

### Processor type and features

**Select** this for a VIA C3 "Nehemiah". **Selecting** this enables usage of SSE and tells ... This device gives processes access to the x86 **CPUID** instruction to be ...

[tlug.up.ac.za/old/guides/lkcg/arch\\_i386.html](http://tlug.up.ac.za/old/guides/lkcg/arch_i386.html) - 42k - [Cached](#) - [Similar pages](#)

